



**VIKRAMA SIMHAPURI UNIVERSITY
KAKUTUR (V), SPSR NELLORE**

No. VSU/B.Tech. Supple./Exams/Dec-2017

Dated : 08.12.2017

III B.Tech. I Semester Supple. Examinations December 2017

TIME -TABLE

Time : 9.00 am to 12.00 Noon

Date	Subjects
21-12-2017 (Thursday)	Structural Analysis (CE) Linear Control Systems (EEE, ECE & EICE) Electrical Engineering (ME) Theory of Computation (CSE)
23-12-2017 (Saturday)	R.C.C. Structural Design – I (CE) Pulse & Digital Circuits (EEE, ECE & EICE) Electronics Engineering (ME) Operating Systems (CSE)
27-12-2017 (Wednesday)	Steel Structural Design (CE) Electrical & Electronic Measurements (EEE, ECE) Economics & Accountancy (ME) Design and Analysis of Algorithms (CSE) Electrical Measurements & Instrumentation (EICE)
29-12-2017 (Friday)	Soil Mechanics (CE) Linear IC Applications (EEE & EICE) Digital Communications (ECE) Applied Thermodynamics – II (ME) Database Management Systems (CSE)
03-01-2018 (Wednesday)	Highway Engineering – II (CE) Power Systems - I (EEE) Antennas and Wave Propagation (ECE) Industrial Engineering & Management (ME) Advanced Computing Technologies (CSE) Communication Engineering (EICE)
05-01-2018 (Friday)	Open Channel Hydraulics (CE) Electromechanical Energy Conversion – III (EEE) Analog IC Applications (ECE) Kinematics of Machinery (ME) Data Communication (CSE) Microprocessors and Microcontrollers (EICE)

NOTE:

The Principals/Head of the Departments/Correspondents are requested to go through the Time Table and the Title of the Papers with respective Q.P Codes and confirm the Time Table, if there is any discrepancies in the Time Table, that may be brought to the notice of the Controller of Examination immediately.

Sd/-
Controller of Examinations(I/c)